

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		((circuit adj board) and semiconductor and bump\$ and adhesive and resin) and electro\$	USPAT; JPO	OR	OFF	2001/09/17 11:55
		((((printed adj circuit adj board) and support) and rail\$) and rod\$	USPAT; JPO	OR	OFF	2001/01/16 07:15
L1	50	29/830.ccls. and (circuit adj board) with support	USPAT; JPO	OR	OFF	2005/08/17 05:27
L2	0	1 and (rails with biasing)	USPAT; JPO	OR	OFF	2005/08/17 05:27
L3	50	29/830.ccls. and ((circuit adj board) with support)	USPAT; JPO	OR	OFF	2005/08/17 05:27
L4	5	3 and rails	USPAT; JPO	OR	OFF	2005/08/17 05:27
L5	0	4 and biasing	USPAT; JPO	OR	OFF	2005/08/17 05:27
S1	22614	"269"/\$.ccls.	USPAT	OR	OFF	2001/09/18 11:37
S2	7	"269"/\$.ccls. and (printed adj circuit adj board adj support)	USPAT; JPO	OR	OFF	2001/01/16 06:01
S3	37	(printed adj circuit adj board adj support) and rail\$	USPAT; JPO	OR	OFF	2001/01/16 06:04
S4	293	printed adj circuit adj board adj support	USPAT; JPO	OR	OFF	2001/01/16 07:12
S5	62275	printed adj circuit adj board	USPAT; JPO	OR	OFF	2004/05/11 14:19
S6	20034	(printed adj circuit adj board) and support	USPAT; JPO	OR	OFF	2005/08/16 14:48
S7	2274	((printed adj circuit adj board) and support) and rail\$	USPAT; JPO	OR	OFF	2001/01/16 07:14
S8	479	(((((printed adj circuit adj board) and support) and rail\$) and rod\$) and (block or table)	USPAT; JPO	OR	OFF	2001/01/16 08:14
S9	53	peckham.in.	USPAT	OR	OFF	2001/01/16 07:42
S10	7	peckham.in. and mark	USPAT	OR	OFF	2001/01/16 07:41
S11	0	teitenberg.in.	USPAT	OR	OFF	2001/01/16 07:41
S12	0	meridian.in.	USPAT	OR	OFF	2001/01/16 07:42
S13	1686	tony.in.	USPAT	OR	OFF	2001/01/16 07:42
S14	0	tony.in. and teitenberg	USPAT	OR	OFF	2001/01/16 07:42
S15	0	peckham.in. and (printed adj circuit adj board adj support)	USPAT	OR	OFF	2001/01/16 07:42
S16	0	tony.in. and (printed adj circuit adj board adj support)	USPAT	OR	OFF	2001/01/16 07:43
S17	233	269/903.ccls. and support	USPAT	OR	OFF	2001/01/16 08:25

S18	80	(269/903.ccls. and support) and (circuit adj board)	USPAT	OR	OFF	2001/01/16 08:25
S19	875	(circuit adj board) and semiconductor and bump\$ and adhesive and resin	USPAT; JPO	OR	OFF	2001/09/18 15:16
S20	3	"6034437".URPN.	USPAT	OR	OFF	2001/09/17 11:17
S21	3	"6034437".URPN.	USPAT	OR	OFF	2001/09/17 11:19
S22	8	("5352926"   "5523622"   "5703405"   "5786271"   "5834844"   "5844304"   "5886409"   "5886415").PN.	USPAT	OR	OFF	2001/09/17 11:19
S23	1	("6274389").PN.	USPAT; USOCR	OR	OFF	2004/05/11 14:19
S24	0	"6274389".URPN.	USPAT	OR	OFF	2004/05/11 14:19
S25	22	("4159976"   "4181645"   "4866108"   "5120665"   "5137936"   "5250848"   "5274913"   "5355580"   "5371328"   "5423931"   "5512613"   "5560934"   "5567266"   "5760337"   "5783867"   "5821456"   "5872158"   "5912282"   "5914381"   "5932682"   "5973033"   "6074895").PN.	USPAT	OR	OFF	2001/09/18 11:39
S26	922	29/271,289.1,760.ccls.	USPAT	OR	OFF	2004/05/11 14:19
S27	184141	PCB or (print\$2 adj circuit adj board)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:20
S28	785	print\$2 adj circuit adj board adj support	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:20
S29	785	(PCB or (print\$2 adj circuit adj board)) and (print\$2 adj circuit adj board adj support)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:20
S30	3	29/271,289.1,760.ccls. and ((PCB or (print\$2 adj circuit adj board)) and (print\$2 adj circuit adj board adj support))	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:21
S31	1	(US-6412768-\$.did.	USPAT	OR	OFF	2004/05/11 14:21
S32	1	((US-6412768-\$.did.) and (contact adj surface)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 15:15
S33	1	(29/271,289.1,760.ccls. and ((PCB or (print\$2 adj circuit adj board)) and (print\$2 adj circuit adj board adj support))) and (contact adj surface)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:22

S34	1	((US-6412768-\$).did.) and (positioning adj surface\$1)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:25
S35	17	"3930644".URPN.	USPAT	OR	ON	2004/05/11 14:41
S36	3	"5899446".URPN.	USPAT	OR	ON	2004/05/11 14:42
S37	39	(print\$2 adj circuit adj board adj support) and bias\$3 and stop	USPAT; JPO; DERWENT	OR	ON	2004/05/11 15:06
S38	1	((US-6412768-\$).did.) and (stop)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 15:17
S39	0	((US-6412768-\$).did.) and (limit\$3)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 15:17
S40	5	(US-3826483-\$ or US-3930644-\$ or US-5694843-\$ or US-5707052-\$ or US-5899446-\$).did.	USPAT	OR	ON	2005/04/06 11:05
S41	86	(position\$3 adj device) with PCB	USPAT; JPO; DERWENT	OR	ON	2005/04/06 11:06
S42	68	S41 and @ad<"19990901"	USPAT; JPO; DERWENT	OR	ON	2005/04/06 11:06
S43	6	("3889936"   "4159505"   "4976415"   "5711065"   "6161811"   "6251493").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/06 11:08
S44	2	("4980971"   "5084959").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/06 11:11
S45	8	(US-3826483-\$ or US-3930644-\$ or US-5694843-\$ or US-5707052-\$ or US-5899446-\$).did. or (EP-271006-\$ or JP-06244597-\$ or JP-08097600-\$).did.	USPAT; DERWENT	OR	ON	2005/08/16 12:57
S46	10452	(circuit adj board) with support	USPAT; JPO	OR	OFF	2005/08/17 05:26
S47	141	(circuit adj board) with support with rails	USPAT; JPO	OR	OFF	2005/08/16 14:49
S48	2	((circuit adj board) with support with rails) same biasing	USPAT; JPO	OR	OFF	2005/08/16 14:51
S49	13	("3826483"   "3838777"   "3853379"   "3930644"   "4030717"   "4184599"   "4667868"   "4672735"   "5067648"   "5694843"   "5775568"   "5820013"   "5922230").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/16 14:50

S50	104	(circuit adj board) with support same biasing	USPAT; JPO	OR	OFF	2005/08/16 14:52
S51	14	S50 and rails	USPAT; JPO	OR	OFF	2005/08/16 14:54
S52	7	rails with biasing with board	USPAT; JPO	OR	OFF	2005/08/16 15:11
S53	18	("3826483"   "3838777"   "3853379"   "3930644"   "4030717"   "4184599"   "4667868"   "4672735"   "5067648"   "5694843"   "5775568"   "5820013"   "5899446"   "5922230"   "6148511"   "6161277"   "6327776"   "6481099").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/16 14:58
S54	26397	(circuit adj board) with (support\$3 or hold\$3)	USPAT; JPO	OR	OFF	2005/08/16 15:11
S55	769	rails with biasing	USPAT; JPO	OR	OFF	2005/08/17 05:27
S56	10	S54 and S55	USPAT; JPO	OR	OFF	2005/08/16 15:11